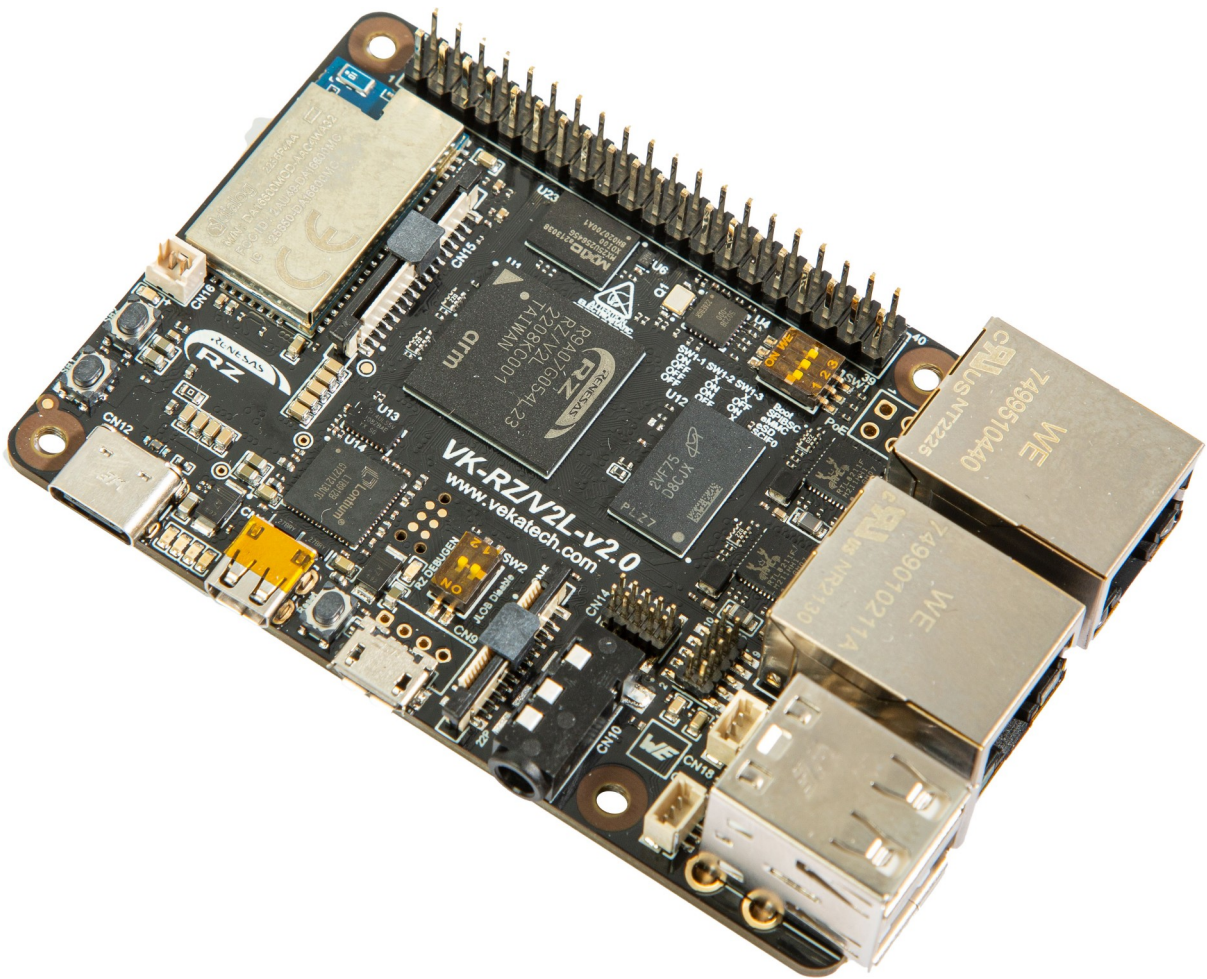


## VK-RZ/V2L DIP Switch Settings



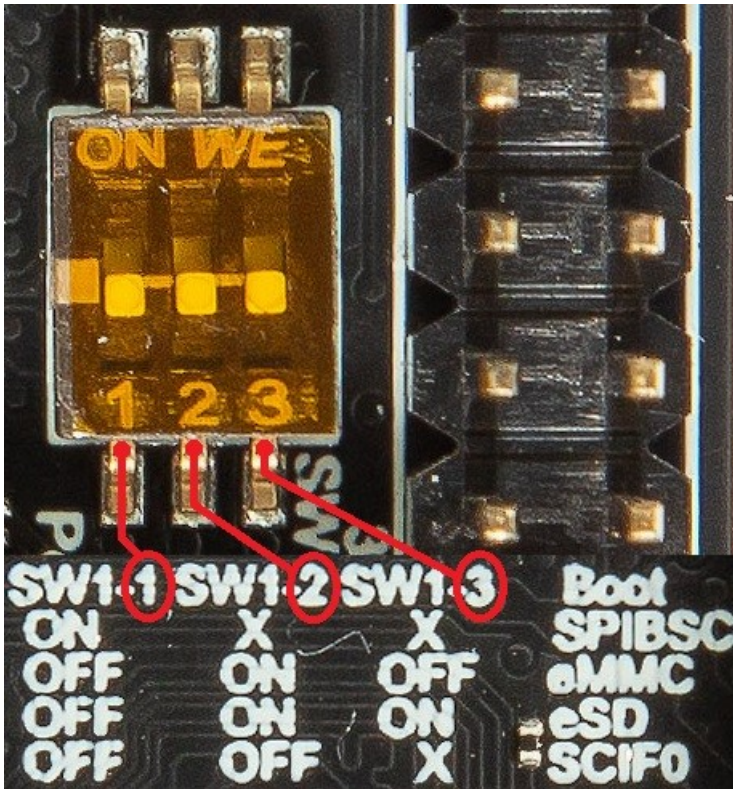
VK-RZ/V2L v2.0 Board



# User's manual

## 1. DIP Switches SW1 & SW2

SW1 controls the boot mode of the board and SW2 controls the debug mode of the board:



SW1 Boot options



SW2 Debug options

<b>[SW1] BOOT from:</b>	<b>1</b>	<b>2</b>	<b>3</b>
QSPI	ON	X	X
SSD Disk	OFF	ON	OFF
SD Card	OFF	ON	ON
System TTY ( $\mu$ USB)	OFF	OFF	X

<b>[SW2] Debug control:</b>	<b>1</b>	<b>2</b>
RZ Debug <b>Disable</b>	ON	–
RZ Debug Enable	OFF	–
J-Link OB <b>Enable</b>	–	ON
J-Link OB Disable	–	OFF

Make sure SW2 pin 1 stays OFF (i.e. RZ Debug is Enabled), if you want to debug RZ MCU through the SWD **J-Link OnBoard** or the JTAG **External** Debugger! Keep in mind that J-Link OB can access only the Cortex-M33 part of the RZ. For full access you will need a JTAG! (i.e. External Debugger on CN14 and Disabled J-Link OB [SW2 pin2  $\rightarrow$  OFF]).



# User's manual

## Revision overview list

Revision number	Description changes
1.0	Initial

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